



JC10 Rec'd PCT/PTO 06 JUN 2005

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q85660

Heiji WATANABE et al.

Appln. No.: 10/521,311

Group Art Unit: Not yet assigned

Confirmation No.: Not yet assigned

Examiner: Not Yet Assigned

Filed: January 14, 2005

For: SEMICONDUCTOR DEVICE, AND MANUFACTURING METHOD AND
MANUFACTURING APPARATUS OF THE SAME

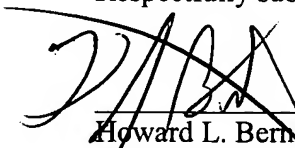
**SUBMISSION OF INTERNATIONAL PRELIMINARY
EXAMINATION REPORT (IPER)**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

For the Examiner's convenience, enclosed herewith is a copy of the English translation of the International Preliminary Examination Report (IPER). It is assumed that copies of the cited references as required by §371(c) will be supplied directly by the International Bureau, but if further copies are needed, the undersigned will undertake to provide them upon request.

Respectfully submitted,


Howard L. Bernstein
Registration No. 25,665

SUGHRUE MION, PLLC
Telephone: (202) 293-7060
Facsimile: (202) 293-7860

WASHINGTON OFFICE

23373

CUSTOMER NUMBER

Date: June 6, 2005